

| No          | Napi Slot DEV PIN | Func2       | Func3               | Func4          | GPIO RK3308 |
|-------------|-------------------|-------------|---------------------|----------------|-------------|
| Side TOP    |                   |             |                     |                |             |
| 1           | VDD_3V3           |             |                     |                |             |
| 3           | VDD_3V3           |             |                     |                |             |
| 5           | VDD_3V3           |             |                     |                |             |
| 7           | I2C1_SCL          |             |                     |                | GPIO0_B4    |
| 9           | I2C1_SDA          |             |                     |                | GPIO0_B3    |
| 11          | I2C2_SCL          | UART0_RTSN  | SPI0_CSN0           |                | GPIO2_A3    |
| 13          | I2C2_SDA          | UART0_CTSN  | SPI0_CLK            |                | GPIO2_A2    |
| 15          | UART0_TX          |             |                     |                | GPIO2_A1    |
| 17          | GND               |             |                     |                |             |
| 19          | UART4_TX          |             |                     |                | GPIO4_B1    |
| 21          | UART4_RX          |             |                     |                | GPIO4_B0    |
| 23          | RESET in          |             |                     |                |             |
| 25          | MAC_MDIO          | LCDC_D10    | I2S1_8CH_SCLK_RX_M1 | PDM_8CH_CLK_M1 | GPIO1_B6    |
| 27          | MAC_TXEN          | LCDC_D13    | I2S1_8CH_SDO0_M1    |                | GPIO1_C1    |
| 29          | MAC_RXDV          | LCDC_D12    | I2S1_8CH_LRCK_RX_M1 |                | GPIO1_C0    |
| 31          | MAC_MDC           | LCDC_D9     | I2S1_8CH_SCLK_TX_M1 |                | GPIO1_B5    |
| 33          | MAC_CLK           | LCDC_D8     | I2S1_8CH_LRCK_TX_M1 |                | GPIO1_B4    |
| 35          | MAC_RXER          | LCDC_D11    | I2S1_8CH_LRCK_TX_M1 |                | GPIO1_B7    |
| 37          | MAC_RST           |             |                     |                | GPIO0_A7    |
| 39          | SDMMC_DET         |             |                     |                | GPIO0_A3    |
| 41          | SDMMC_CLK         |             |                     |                | GPIO4_D5    |
| 43          | SDMMC_D3          |             |                     |                | GPIO4_D3    |
| 45          | SDMMC_D2          |             |                     |                | GPIO4_D2    |
| 47          | SDMMC_D1          |             |                     |                | GPIO4_D1    |
| 49          | SDMMC_D0          |             |                     |                | GPIO4_D0    |
| 51          | SDMMC_CMD         |             |                     |                | GPIO4_D4    |
| Side BOTTOM |                   |             |                     |                |             |
| 2           | VDD_3V3           |             |                     |                |             |
| 4           | VDD_3V3           |             |                     |                |             |
| 6           | GND               |             |                     |                |             |
| 8           | SPI2_CSN0         | UART1_TX    | I2C0_SCL            |                | GPIO1_D1    |
| 10          | SPI2_MOSI         | UART1_RTSN  | UART2_TX_M0         | JTAG_TMS       | GPIO1_C7    |
| 12          | SPI2_MISO         | UART1_CTSN  | UART2_RX_M0         | JTAG_TCK       | GPIO1_C6    |
| 14          | SPI2_CLK          | UART1_RX    | I2C0_SDA            |                | GPIO1_D0    |
| 16          | UART0_RX          | SPI0_MISO   |                     |                | GPIO2_A0    |
| 18          | GND               |             |                     |                |             |
| 20          | UART3_RX          | I2C3_SDA_M1 | SPI1_MOSI           |                | GPIO3_B4    |
| 22          | UART3_TX          | I2C3_SCL_M1 | SPI1_CSN0           |                | GPIO3_B5    |
| 24          | GND               |             |                     |                |             |
| 26          | MAC_TXD0          | LCDC_D14    | I2S1_SDO1_SDI3_M1   | PDM_SDI3_M1    | GPIO1_C2    |
| 28          | MAC_TXD1          | LCDC_D15    | I2S1_SDO2_SDI2_M1   | PDM_SDI2_M1    | GPIO1_C3    |
| 30          | GND               |             |                     |                |             |
| 32          | MAC_RXD0          | LCDC_D16    | I2S1_SDO3_SDI1_M1   | PDM_SDI1_M1    | GPIO1_C4    |
| 34          | MAC_RXD1          | LCDC_D17    | I2S1_SDI0_M1        | PDM_SDI0_M1    | GPIO1_C5    |
| 36          | GND               |             |                     |                |             |
| 38          | USB0_OTG_P        |             |                     |                |             |
| 40          | USB0_OTG_N        |             |                     |                |             |
| 42          | GND               |             |                     |                |             |
| 44          | USB1_HOST_P       |             |                     |                |             |
| 46          | USB1_HOST_N       |             |                     |                |             |
| 48          | GND               |             |                     |                |             |
| 50          | USB_DET           |             |                     |                |             |
| 52          | USB_DRV           |             |                     |                | GPIO0_C5    |